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Dated: May 17, 2004

Signature: \_\_\_\_\_

(Gregory S. Gewirtz)

Docket No.: INTECH 3.0-047  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Schafbauer et al.

Application No.: 09/826,732

Group Art Unit: 2827

Filed: April 5, 2001

Examiner: A. Chambliss

For: **STRUCTURE AND METHOD FOR  
DETERMINING EDGES OF REGIONS IN A  
SEMICONDUCTOR WAFER**

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This communication is in response to the Office Action mailed April 16, 2004, setting forth a restriction requirement in the above-identified application. In the Office Action, the Examiner required restriction to one of the following inventions under 35 U.S.C. §121:

I. Claims 1-4, drawn to a semiconductor device.

II. Claims 5-20, drawn to a method of making a semiconductor device.

In response, Applicants hereby elect the invention of Group I, corresponding to claims 1-4. Applicants reserve the right to file a divisional application corresponding to the non-elected claims.



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In the event any fee is due in connection with the present response, the Examiner is authorized to charge Applicant's Deposit Account No. 12-1095 therefor.

Dated: May 17, 2004

Respectfully submitted,

By   
\_\_\_\_\_  
Gregory S. Gewirtz

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